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Docket No.: M4065.0624/P624
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes

Application No.: 10/751,439

Confirmation No.: 3917

Filed: January 6, 2004

Art Unit: 2811

For: METHOD AND APPARATUS
PROVIDING CMOS IMAGER DEVICE
PIXEL WITH TRANSISTOR HAVING
LOWER THRESHOLD VOLTAGE THAN
OTHER IMAGER DEVICE TRANSISTORS

Examiner: Not Yet Assigned

STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive an Office Action from the Patent and Trademark Office.

Dated: April 6, 2005

Respectfully submitted,

By 

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